Declaration of Conformity



For the ESB Networks NC6-01-R7 Form:

Micro-Generation Protection Settings Confirmation Certificate.

Applicable to inverters connected to the low voltage grid:

Inverter Types:

SUN2000-2~6KLT-L1 (Single Phase) SUN2000-3~10KTL-M1 (Three Phase) SUN2000-12~20KTL-M2 (Three Phase) SUN2000-12~25KTL-M5 (Three Phase) SUN2000-30~40KTL-M3 (Three Phase)

SUN2000-50KTL-M3 (Three Phase) SUN2000-60KTL-M0 (Three Phase) SUN2000-100KTL-M1 (Three Phase) SUN2000-100KTL-M2 (Three Phase) SUN2000-115KTL-M2 (Three Phase)

Statement:

The above-mentioned Solar Inverters (Power Park Modules, PPMs) are compliant to the technical regulations set forth in the following documents:

- ESB Company Standard DTIS-230206-BRL Conditions Governing the Connection and Operation of Micro-Generation
- IS. EN 50549-1:2019
 Requirements for generating plants to be connected in parallel with distribution networks -Part 1: Connection to a LV distribution network - Generating plants up to and including Type B

Huawei Technologies (Ireland) Co., Ltd.

Signed:

Date:

Runful

Bouke van der Weerdt Solution Sales Director – WEU Multi-Country Digital Energy Business Dept.

2023-Feb-27

Huawei Confidential

72-02-2023

The above-mentioned Solar Inverters (Power Park Modules, PPMs) fulfil the below parameters:

Parameter		Trip setting	Clearance time	Confirm settings have been / will be applied (Y/N)
	Over Voltage	e		and the second
Pre I.S EN 50549-1 Single Stage Voltage Setting		269 V / 468 V	0.7 s	Y
I.S. EN 50549-1 Two Stage Voltage Settings	Stage 1	269 V / 468 V	70 s	Y
	Stage 2	281 V / 488 V	0.7 s	Y
Under voltage		191 V / 332 V	0.7 s	Y
Over frequency		52 Hz	0.5 s	Y
Under frequency		47 Hz	0.5 s	Y

An explicit Loss of Mains functionality shall be included. Established methods such as, but not limited to, Rate of Change of Frequency, or Source Impedance Measurement may be used. Where Source Impedance is measured, this shall be achieved by purely passive means. Any implementation which involves the injection of pulses onto the DSO network, shall not be permitted.

ROCOF	1.0 Hz/s	0.6 s	Y
Vector Shift	Not permitted		Y