

Form A2-3: Compliance Verification Report for Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99.The form can be used in a variety of ways as detailed below:

1. To obtain Fully Type Tested status

The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register.

2. To obtain Type Tested status for a product

This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

3. One-off Installation

This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99. This form must be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module**, **Generating Unit or Inverter** as appropriate for the context. However, note that compliance must be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **FullyType Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the Product ID), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99.

Manufact	urer's reference number		DQ2004001-01
PGM tech	nology		RHI-4.6K-48ES-5G
Manufact	urer name	Ningbo Ginlong	Technologies Co., Ltd.
Address		No. 57 Jintong Xiangshan, Ning 315712,P.R.Chin	
Tel	(+86) 574 6580 3377	Web site	www.ginlong.com
E:mail	kun.zhang@ginlong.com		
Registere	ed Capacity		4.6kVA

Type A Power Generating Modules



There are four options for Testing: (1) **Fully Type Tested**, (2) Partially **Type Tested**, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type TestedPGMs** tests marked with * may be carried out at the time of commissioning (Form A4).

Tested option:	1. Fully Type Tested	2.Partiall y Type Tested	3. One-off Man. Info.	4. Tested on Site at time of Commission- ing
0. Fully Type Tested - all tests detailed below completed and evidence attached to this submission	Yes	N/A	N/A	N/A
1. Operating Range				
2. PQ – Harmonics]			
3. PQ – Voltage Fluctuation and Flicker]			
4. PQ – DC Injection (Power Park Modules only)			×	
5. Power Factor (PF)*			-	
6. Frequency protection tripand ride through tests*				
7. Voltageprotectiontrip and ride through tests*				
8. Protection – Loss of Mains Test*, Vector ShiftandRoCoF Stability Test*	N/A			
9.LFSM-O Test*			1	
10. Protection – Reconnection Timer*				
11. Fault Level Contribution				
12. Self-monitoring Solid State Switch			e	
13. Wiring functional tests if required by para 15.2.1 (attach relevant schedule of tests)*				
14. Logic Interface (input port)*				
* may be carried out at the time of commissioning (Form A.2 Document reference(s) for Manufacturers' Information :	2-4).			

Type A Power Generating Modules



Manufacturer compliance declaration. - I certify that all products supplied by the company with the above**TypeTestedManufacturer's** reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site **Modifications** are required to ensure that the product meets all the requirements of EREC G99.

宁波锦浪新能源科技股份有限公司 NINGBO GINLONG TECHNOLOGIES CO., LID Signed On behalf of Pankadi Manufacturerstamp

Note that testing can be done by the Manufacturer of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.



A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules – test record

1. Operating Range: Two tests should be carried with the **Power Generating Module** operating at **RegisteredCapacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within ± 5 % of the apparent power value set for the entire duration of each test sequence.

Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

The **Interface Protection** shall be disabled during the tests.

In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.

In case of a full converter **Power ParkModule**(eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source.

Test 1 Voltage = 85% of nominal (195.5 V), Frequency = 47 Hz, Power Factor = 1, Period of test 20s	Tested with the specified conditions,in the 20 seconds period of time,the inverters operate normally
Test 2 Voltage = 85% of nominal (195.5 V), Frequency = 47.5 Hz, Power Factor = 1, Period of test 90 minutes	Tested with the specified conditions, in the 90 minutes period of time, the inverters operate normally
Test 3 Voltage = 110% of nominal (253 V)., Frequency = 51.5 Hz, Power Factor = 1, Period of test 90 minutes	Tested with the specified conditions,in the 90 minutes period of time,the inverters operate normally
Test 4 Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, Power Factor = 1, Period of test 15 minutes	Tested with the specified conditions, in the 15 minutes period of time, the inverters operate normally
Test 5 RoCoF withstand Confirm that the Power Generating Module is capable of staying connected to the Distribution Network and operate at rates of change of frequency up to 1 Hzs ⁻¹ as measured over a period of 500 ms. Note that this is not expected to be demonstrated on site.	Tested with the specified conditions, the inverters operate normally



2. Power Quality – Harmonics:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12The results need to comply with the limits of Table2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment.

Power Generating Modules with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the **Power Generating Module** in order to accept the connection to a **Distribution Network**.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC G5.

Power Generating Module tested to BS EN 61000-3-12

	-					
Power Gene phase (rpp)	erating Module ratin	ng per	4.6	kVA	Harmonic % = Measured V (A) x 23/rating per phase (k	
Harmonic	At 45-55% of Re Capacity		100% of Regis Capacity		Limit in BS EN	l 61000-3-12
	Measured Value MV in Amps	%	Measured Value MV in Amps	%	1 phase	3 phase
2	0.071	0.355	0.122	0.610	8%	8%
3	0.067	0.335	0.103	0.515	21.6%	Not stated
4	0.019	0.095	0.03	0.150	4%	4%
5	0.037	0.185	0.063	0.315	10.7%	10.7%
6	0.015	0.075	0.023	0.115	2.67%	2.67%
7	0.047	0.235	0.073	0.365	7.2%	7.2%
8	0.011	0.055	0.018	0.090	2%	2%
9	0.043	0.215	0.061	0.305	3.8%	Not stated
10	0.01	0.050	0.014	0.070	1.6%	1.6%
11	0.055	0.275	0.072	0.360	3.1%	3.1%
12	0.008	0.040	0.012	0.060	1.33%	1.33%
13	0.057	0.285	0.076	0.380	2%	2%
THD ¹		0.743		1.135	23%	13%
PWHD2		0.606		0.958	23%	22%

¹ THD = Total Harmonic Distortion

²PWHD = Partial Weighted Harmonic Distortion



3. Power Quality – Voltage fluctuations and Flicker:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC P28.

	Starting					Stopping			Running		
	d max	d c		d(t)		d max	d c	d(t)	P st	P lt 2	hours
Measured Values at test impedance	0.76%	0.06	6%		0	0.72%	0.04%	0	0.21	0	.21
Normalised to standard impedance	0.76%	0.06	6%		0	0.72%	0.04%	0	0.21	0	.21
Normalised to required maximum impedance	N/A	N/	A	Ν	I/A	N/A	N/A	N/A	N/A	1	V/A
Limits set under BS EN 61000-3-11	4%	3.3	%	3.	.3%	4%	3.3%	3.3%	1.0	0	.65
Test Impedance	•		F	र		0.4	Ω	XI	0.15		Ω
Standard Imped	ance		F	र		0.24 * 0.4 ^	Ω	ХІ	0.15 * 0.25 ^		Ω
Maximum Imped	dance		F	र		N/A	Ω	XI	N/A		Ω

* Applies to three phase and split single phase Power Generating Modules.

^ Applies to single phase **Power Generating Module** and **Power Generating Modules** using two phases on a three phase system

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the **Power Factor** of the generation output is 0.98 or above.

Normalised value = Measured value x reference source resistance/measured source resistance at test point

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is 0.4 Ω

Two phase units in a split phase system reference source resistance is 0.24 Ω

Three phase units reference source resistance is 0.24 Ω



Where the **Power Factor** of the output is under 0.98 then the XI to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to comply with the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below

Test start date	20. Apr.2020	Test end date	25. Apr.2020
Test location	Ningbo Ginlong Technologies	Co.,Ltd.	

4. Power quality – DC injection: The tests should be carried out on a single **Generating Unit**. Tests are to be carried out at three defined power levels ±5%. At 230V a 4.6kW single phase **Inverter** has a current output of 20.0A so DC limit is 50.0mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.

Test power level	10%	55%	100%
Recorded value in Amps (mA)	23.15	29.23	32.18
as % of rated AC current	0.12	0.15	0.16
Limit	0.25%	0.25%	0.25%

5. Power Factor: The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity**. Voltage to be maintained within $\pm 1.5\%$ of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	0.9993	0.9990	0.9994
Power FactorLimit	>0.95	>0.95	>0.95

6. Protection – Frequency tests:These tests should be carried out in accordance with the Annex A.7.1.2.3.

Function	Sett	ting	Trip te	st	"No trip te	sts"
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5Hz	20 s	47.51Hz	20.033s	47.7Hz 30s	Yes
U/F stage 2	47Hz	0.5 s	47.02Hz	0.534s	47.2Hz 19.5s	Yes
					46.8Hz 0.45s	Yes
O/F	52Hz	0.5 s	51.99Hz	0.533s	51.8Hz	Yes



	120s	
	52.2Hz 0.45s	Yes

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: These tests should be carried out in accordance with Annex A.7.1.2.2.

Function	Se	etting	Trip te	est	"No trip tests"	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V	0.8 pu (184V)	2.5s	183.8V	2.535s	188V 5s	Yes
					180V 2.45s	Yes
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
O/V stage 1	1.14 pu (262.2V)	1.0s	262.6V	1.041s	258.2V 5.0s	Yes
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
O/V stage	1.19 pu (273.7V)	0.5s	274.0V	0.541s	269.7V 0.95s	Yes
					277.7V 0.45s	Yes

Note for Voltage tests the Voltage required to trip is the setting ± 3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ± 4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8.Protection – Loss of Mains test: These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4.

The following sub set of tests should be recorded in the following table.

Test Power and imbalance 33% 66% 100% 33% 66% 100% -5% Q -5% Q -5% P +5% Q +5% Q +5% P

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	Test 2	22	Test 12	Test 5	Test 31		Test 21		Test 10		
Trip time. Limit is 0.5s	0.2	9s	0.40s	s 0.42s		0.28	6	0.38s		0.42s	
Loss of Mains P Annex A.7.1.2.6.	rotecti	ion, Ve	ector Shift S	Stability	test. This	test should	be car	ried out	in ad	ccordance with	
Sta		art Frequency		Change		Co		nfirm no trip			
Positive Vector Shift		49.5Hz		+50 degrees				Υe	Yes		
Negative Vector Shift		50.5Hz		- 50 degrees				Ye	Yes		
Loss of Mains P A.7.1.2.6.	rotecti	on, Ro	CoF Stabili	ty test: ⁻	This test sh	ould be ca	rried ou	t in acco	ordar	nce with Annex	
Ramp range		Test frequency ramp		ramp:	Test Duratior			Confirm no trip			
49.0Hz to 51.0Hz			+0.95Hzs ⁻¹			2.1 s			Yes		
51.0Hz to 49.0Hz			-0.95Hzs ⁻¹			2.1 s			Yes		
This test should be carried out in accordance with Annex A.7.1.3. Active Power response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4. Yes Alternatively, simulation results should be noted below: Image: Comparison of the c											
		Measured A Power Outp		Frequency		Primary Power Source			Active Power Gradient		
Step a) 50.00Hz	z ±0.01	Hz	4612	N	50.0)0Hz				-	
Step b) 50.45Hz).45Hz ±0.05Hz 4609W 50.43		15Hz			-					
Step c) 50.70Hz	z ±0.10	Hz	4157	N	50.70Hz				-		
Step d) 51.15Hz) 51.15Hz ±0.05Hz 3318W 51.15H		5Hz 4850W		50W	-					
Step e) 50.70Hz	z ±0.10	Hz	4155	N	50.70Hz				-		
Step f) 50.45Hz	±0.05	Hz	4609	N	50.4	50.45Hz			-		
Step g) 50.00Hz	z ±0.01	Hz	4608	N	50.0	00Hz			27.6kW/min		
Test sequence at Registered Capa 60%			Measured A Power Outp		Frequency		Primary Power Source			Active Power Gradient	



Step a) 50.00Hz ±0	2307W		50.00Hz					-		
Step b) 50.45Hz ±0.05Hz		2301W		50.45Hz		2430W		-		
Step c) 50.70Hz ±0.10Hz		1838W		50.70Hz				-		
Step d) 51.15Hz ±0.05Hz		1010W		51.15Hz					-	
Step e) 50.70Hz ±0	1839W		50.70Hz				-			
Step f) 50.45 Hz ±0	2302W		50.45Hz		4850W		0kW/min			
Step g) 50.00 Hz ±0	4617W		50.00Hz		4850W		27.6kW/min			
10. Protection – Re-	connection ti	mer.					·			
Test should prove th voltage and frequence						n dela	ay of 20 s f	or re	storation of	
Time delay setting	Measured de	Checks on no reconnection brought to just outside stage 1				when voltage or frequency is I limits of Table 10.1.				
30s	37s		At 1.16 pu (266.2 V)		At 0.78 pu (180V)		At 47.4 Hz		At 52.1 Hz	
Confirmation that the Module does not re-	ating	Yes		Yes		Yes		Yes		
11. Fault level contribution : These tests shall be carried out in accordance with EREC G99 Annex A.7.1.5.								nex A.7.1.5.		
For Inverter output										
Time after fault	Volts	Volts				Amps				
20ms	52.2V	52.2V				25.1A				
100ms	51.7V	51.7V				0A				
250ms	51.3V	51.3V				0A				
500ms	51.3V				(0A				
Time to trip	0.062s	0.062s				In seconds				
12. Self-Monitoring solid state switching: No specified test requirements.Referto Annex A.7.1.7.										
It has been verified that in the event of the solid state switching device failing to disconnect the Power Park Module , the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s. N/A (Solid state switch means electronic switch, Solis inverter uses mechanical dual relay protection with relay checks, which drops the voltage below 50V in 0.5s)										
13. Wiring functional tests: If required by para 15.2.1.										

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Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	N/A(Not applicable. Refer to 15.2.1, inverter is using special connector for wiring)						
14. Logic interface (input port).							
Confirm that an input port is provided and can be used to shut down the module.	Yes (Logic interface is marked as "DRM" either on inverter or on external DRM device depending on inverter model. Please see inverter or external DRM device manual for detail.						
Additional comments.							